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PATENT NUMBER

**O.I.P.E.**

PATENT DATE

FR KCZ TM  
SCANNED Q.A.

APPLICATION NO. 09/956986	CONT/PRIOR F	CLASS 257	SUBCLASS	ART UNIT 2811	EXAMINER
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## APPLICANTS

Michiharu Matsui  
Seiichi Mori  
Riichiro Shirota  
Yuji Takeuchi

# TITLE

Nonvolatile semiconductor memory device having element isolating region of trench type and method of manufacturing the same

PTO-2040  
12/99

<b>ISSUING CLASSIFICATION</b>															
<b>ORIGINAL</b>				<b>CROSS REFERENCE(S)</b>											
<b>CLASS</b>		<b>SUBCLASS</b>		<b>CLASS</b>	<b>SUBCLASS (ONE SUBCLASS PER BLOCK)</b>										
<b>INTERNATIONAL CLASSIFICATION</b>															

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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